IN THE ABSTRACT

Please rewrite the abstract as shown below.

~~**MONOTONOUS UP-COUNTER IN AN INTEGRATED CIRCUIT**~~

ABSTRACT

An increasing ~~monotonous~~ monotonic counter over n bits formed as an integrated circuit, ~~comprising:~~ including an assembly of $2^{n+1}-(n+2)$ irreversible counting cells distributed in at least n groups of 2^p-1 counting cells, where p designates the group rank~~;~~, and at least n-1 parity calculators, each calculator providing a bit of rank p, increasing from the most significant bit of the result count, taking into account the states of the cells of the group of same rank.